


N. Homma, Y. Hayashi, N. Miura, D. Fujimoto, M. Nagata, and T. Aoki, "Design Methodology and Validity Verification for a

(a-2) Conference Proceedings


(b-1) Book Chapter


(b-2) Invited Paper


(e-1) Invited Talk

(d-1) Award

(e-1) Academic Activity
[135] Symposium on VLSI Circuits 2014-2017, Technical Program Committee (TPC) Member
[139] Asian South Pacific Design Automation Conference (ASP-DAC) 2015, University Design Contest Co-Chair
[140] Asian South Pacific Design Automation Conference (ASP-DAC) 2014-2017 University Design Contest Committee Member